

REMARKS

Claims 6, 7, 10, 11, 18, 19, 22, and 23 remain in the application for consideration of the Examiner.

Reconsideration and withdrawal of the outstanding rejections are respectfully requested in light of the above amendments and following remarks.

Claims 6 and 7 were rejected under 35 U.S.C. § 112, second paragraph, as being indefinite.

Claim 6 has been amended to take into consideration the helpful comments of the Examiner as set forth in the Office Action.

It is respectfully submitted that Claims 6 and 7 are in full compliance with 35 U.S.C. § 112, second paragraph, and particularly points out and distinctly claims the subject matter which Applicants believe is their invention.

Claims 1-5, 8-9, 12-17, 20-21, and 24-25 were rejected under 35 U.S.C. § 102(b) as being anticipated by Flannagan; and Claims 6-7, 10, 11, 18, 19, 22, and 23 were rejected under 35 U.S.C. § 103 as being unpatentable over Flannagan.

These rejections are respectfully traversed.

It is respectfully submitted that Flannagan does not disclose or suggest the presently claimed invention including the second (third) transistor receiving a second (third) bias voltage in independent Claims 6 and 18, and the resistor as set forth in Claims 10 and 22.

The Examiner alleges that the specific limitations drawn to choice of connection and the allegation that one of ordinary skill in the art would have inserted the resistor in the various ways as set forth in the claimed subject matter.

Applicants traverse this allegation and request a teaching from the prior art.

Applicants appreciate the indication that if Claims 6 and 7 were amended to comply with 35 U.S.C. § 112, Claims 6 and 7 would be allowed.

Additionally, Applicants appreciate the indication that Claims 18 and 19 are allowed.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



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